

Form PTO-1449 Modified List of Patent and Publications Cited by Applicant (Use several sheets if necessary) U.S. Department of Commerce Patent and Trademark Office	Docket No. MSFT-3008 / 304862.03	Application No. 10/763,778
	Applicant Anuj B. Gosalia and Steve Pronovost	
	Filing Date January 22, 2004	Group 2127
	Confirmation No. 7563	

U. S. PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Name	Class	Subclass
/C.A./	1	5,777,629	07/07/98	Baldwin	345	506
/C.A./	2	5,896,141	04/20/99	Blaho et al.	345	541
/C.A./	3	5,918,050	06/29/99	Rosenthal et al.	718	108
/C.A./	4	6,023,738	02/08/00	Priem et al.	710	23
/C.A./	5	6,049,857	04/11/00	Watkins	711	207
/C.A./	6	6,065,071	05/16/00	Priem et al.	710	22
/C.A./	7	6,081,854	06/27/00	Priem et al.	710	37
/C.A./	8	6,252,600	06/26/01	Kohli et al.	345	219
/C.A./	9	6,437,788	08/20/02	Milot et al.	345	552
/C.A./	10	6,446,186	09/03/02	Priem et al.	711	206
/C.A./	11	6,518,973	02/11/03	Blythe	345	564
/C.A./	12	6,691,180	02/10/04	Priem et al.	710	23
/C.A./	13	6,798,421	09/28/04	Baldwin	345	557

FOREIGN PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Country	Translation	
					YES	NO
/C.A./	14	WO 02/09083	01/31/02	PCT		

EXAMINER	DATE CONSIDERED
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NON-PATENT DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
/C.A./	15	Intel Corp, "Accelerated Graphics Port Interface Specification, Revision 1.0," July 1996, 1-161	
/C.A./	16	Martin, K.E. et al., "Direct Rendering Infrastructure, Low-Level Design Document," http://dri.sourceforge.net/doc/design_low_level.html , May 1999, 1-18	
/C.A./	17	Owen, J. et al., "A Multiple Direct Rendering Architecture for 3D," http://dri.sourceforge.net/doc/design_high_level.html , September 1998, 1-10	
/C.A./	18	Torvalds, L. et al., "Excerpt from the Linux Kernel, version 2.4.20," http://www.kernel.org/pub/linux/kernel/v2.4/linux-2.4.20.tar.bz2 , November 2002, 1-87	

EXAMINER	/Caroline Arcos/	DATE CONSIDERED	10/15/2007
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		Filing Date Herewith	Group Not Yet Assigned
		Confirmation No. Not Yet Assigned	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
/C.A./	1	Burgess, P. et al., "BED: a multithreaded kernel for embedded systems," <i>Proceedings of the IFAC Workshop on Real Time Programming</i> , June 22-24, 1994, 133-138	
/C.A./	2	Macedonia, M., "The GPU enters computing's mainstream," <i>Computer</i> , 2003, 36(10), 106-108	
/C.A./	3	Pétrot, F. et al., "Lightweight Implementation of the POSIX Threads API for an On-Chip MIPS Multiprocessor with VCI Interconnect," <i>Proceedings Design, Automation and Test in Europe Conference and Exhibition</i> , March 3-7, 2003, Suppl., 51-56	
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